PATENT APPLICATION
Docket N . 9903-071
Client No. S02US035

Applicant: Jin-Hyuk LEE, Gu-Sung KIM, Dong-Ho LEE and Dong-Hyeon JANG

Serial No: not yet assigned Examiner: not yet assigned

Filing Date: October 21, 2003 Group: not yet assigned

Title: METHOD FOR MANUFACTURING A WAFER LEVEL CHIP SCALE PACKAGE

INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)

U.S. PATENT DOCUMENTS

Exam Init	Ref	Document Number	Issue Date	Name	Class	Sub Class

FOREIGN PATENT DOCUMENTS

Exam Init	Ref	Document Number	Publication Date	Country	Name
		2001-04529	1/15/2001	Korea	Sun-Jin Cho
		2000-65487	11/15/2000	Korea	Lee and Sim
		2000-286283	10/13/2000	Japan	Kuniyasu

OTHER DOCUMENTS

Exam Init	Ref	Author, Title, Date, Pertinent Pages, Etc.)
		English language of Korean Abstract for Korean Patent Publication No. 2001-04529, filed 1/15/2001.
		English language of Korean Abstract for Korean Patent Publication No. 2000-65487, filed 11/15/2000.
		English language of Japanese Abstract for Japanese Patent Publication No. 2000-286283, filed 10/13/2000.

Examiner: _____

Date Considered: 6-7-5